

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Mark D. Eason, Harry Rodriguez and Mark R. Muegge  
APPLICATION NO.: 10/735,211  
FILING DATE: December 12, 2003  
TITLE: Power Converter Controller Having Event Generator for Detection  
of Events and Generation of Digital Error  
EXAMINER: Unknown  
GROUP ART UNIT: 2838  
ATTY. DKT. NO.: 23516-07980

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

Dated: April 7, 2004

By: 

Jae Won Song, 37 C.F.R. §10.9(b)

COMMISSIONER FOR PATENTS  
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**INFORMATION DISCLOSURE STATEMENT**

**Under 37 CFR §§ 1.56 and 1.97-98**

SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO/SB/08A listing references for consideration by the Examiner. Enclosed is a copy of each non-patent cited reference that may be material to the examination of this application, and for which there may be a duty to disclose.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

- ☒ within three months of the filing date of the application, or date of entry into the national stage of an international application, or before the mailing date of a first office action on the merits, whichever event last occurred;

- ☐ before the mailing of a first official action after the filing of a request for continued examination (RCE) under 37 CFR § 1.114;
- ☐ after three months of the filing date of this national application or the date of entry of the national stage in an international application, or after the mailing date of the first official action on the merits, whichever event last occurred, but before the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; or (2) an action that otherwise closes prosecution in the application, and:
  - ☐ attached hereto is the fee set forth under 37 CFR § 1.17(p) for submission of this Information Disclosure Statement under 37 CFR § 1.97(c); OR
  - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
    - ☐ each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; OR
    - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement;
- ☐ on or before the payment of the issue fee but after the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; (2) a notice of allowance under 37 CFR § 1.311; or (3) an action that otherwise closes prosecution in the application, and:
  - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
    - ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or
    - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a


foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement; AND

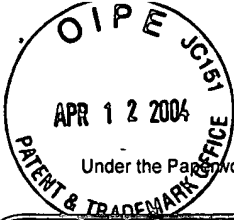
- ☐ attached hereto is the fee set forth under 37 CFR §1.17(p) for submission of this Information Disclosure Statement under 37 CFR. § 1.97(c); OR
- ☐ after the payment of the issue fee. Applicant requests that the information contained in this Information Disclosure Statement be placed in the file according to 37 CFR § 1.97(i), although the information may not be considered by the USPTO.
- ☐ This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior application No. APPLICATION NO., filed on DATE FILED, and the references cited therein are hereby referenced, but are not required to be provided in this application under 37 CFR § 1.98(d).
- ☐ Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 CFR § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement. 37 CFR § 1.704(d).
- ☒ Applicant submits that no fee is required for the consideration of this Information Disclosure Statement.

Consideration of the listed references and favorable action are solicited.

Respectfully submitted,  
MARK D. EASON, HARRY RODRIGUEZ AND  
MARK R. MUEGGE

Dated: April 7, 2004

By:   
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PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

### Complete if Known

Application No.	10/735,211
Filing Date	December 12, 2003
First Named Inventor	Mark D. Eason
Art Unit	2838
Examiner Name	Unknown
Attorney Docket Number	23516-07980

Sheet 1 of 2

### U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document No. Number – Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
	A	US-6,621,255 B2	9-16-2003	Telefus
	B	US-6,434,021 B1	8-13-2002	Collmeyer, et al.
	C	US-6,404,173 B1	6-11-2002	Telefus
	D	US-6,385,059 B1	5-07-2002	Telefus, et al.
	E	US-6,381,150 B2	4-30-2002	Telefus
	F	US-6,370,039 B1	4-09-2002	Telefus
	G	US-6,324,079 B1	11-27-2001	Collmeyer, et al.
	H	US-6,304,473 B1	10-16-2001	Telefus, et al.
	I	US-6,275,018 B1	8-14-2001	Telefus et al.
	J	US-6,038,146	3-14-2000	Luo, et al.
	K	US-5,991,172	11-23-1999	Jovanovic, et al.
	L	US-5,841,643	11-24-1998	Schenkel
	M	US-5,751,561	5-12-1998	Ho, et al.
	N	US-5,438,499	8-01-1995	Bonte, et al.
	O	US-5,305,192	4-19-1994	Bonte, et al.
	P	US-5,138,543	8-11-1992	Harm, et al.

### OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>6</sup>
	Q	BIBIAN, STEPHANE et al., "Digital Control with Improved Performance for Boost Power Factor Correction Circuits," 16 <sup>th</sup> Annual IEEE Applied Power Electronics Conference and Exposition, March 2001, Vol. 1, pp. 137-143.	
	R	ERICKSON, ROBERT et al., "Design of a Simple High-Power-Factor Rectifier Based on the Flyback Converter," Proceedings of IEEE Applied Power Electronics Conference 1990, March 1990, pp. 792-801.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

23516/07980/DOCS/1412783.1

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application No.	10/735,211
				Filing Date	December 12, 2003
				First Named Inventor	Mark D. Eason
				Art Unit	2838
				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	23516-07980

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	S	MURPHY, P. et al., "Digital Control of Power Factor Correction," Power Electronics-Annual Seminar, 2003, pp. 341-347.	
	T	ZHANG, WANFENG et al., "A New Predictive Control Strategy for Power Factor Correction," 18 <sup>th</sup> Annual IEEE Applied Power Electronics Conference and Exposition, February 2003, Vol. 1, pp. 403-409.	
	U	"NCP1651 – Product Review, Single Stage Power Factor Control," April 2002, Rev. 1, Semiconductor Components Industries, LLC.	
	V	"NCP1651 - Single Stage Power Factor Control" [online], October 2003, Rev. 5, ON Semiconductor Components Industries LLC, [retrieved January 26, 2004]. Retrieved from Internet: <URL: <a href="http://www.onsemiconductor.com/pub/Collateral/NCP1651-D.pdf">http://www.onsemiconductor.com/pub/Collateral/NCP1651-D.pdf</a> >.	
	W	"AND8124/D – 90W, Universal Input, Single Stage, PFC Converter" [online], December 2003, ON Semiconductor Components Industries LLC, [retrieved January 26, 2004]. Retrieved from Internet: <URL: <a href="http://www.onsemiconductor.com/pub/Collateral/AND8124-D.pdf">http://www.onsemiconductor.com/pub/Collateral/AND8124-D.pdf</a> >.	
	X	"Unitrode UC1854, UC2854, UC3854 High Power Factor Preregulator," June 1998, Texas Instruments Incorporated.	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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